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Patricia DiGregorio	(Depositor's name)
Vatrice Di Legario	(Signature)
June 16, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/552,085	02/24/2000	Tsutomu Ishikawa		6265

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PADS WITH LESS INPUT SIGNAL ATTENUATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE PUBLICATION FEE		TOTAL FEE(S) DUE	DATE DUE	
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